

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10689981
<b>Filing Date:</b>	20-Oct-2003
<b>Title of Invention:</b>	Integrated circuit devices including low dielectric side wall spacers and methods of forming same
<b>First Named Inventor/Applicant Name:</b>	Beom-jun Jin
<b>Filer:</b>	Timothy Jennings Wall/Michele McMahan
<b>Attorney Docket Number:</b>	5649-1135

Filed as Large Entity

### **Utility Filing Fees**

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
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**Basic Filing:**

**Pages:**

**Claims:**

**Miscellaneous-Filing:**

**Petition:**

**Patent-Appeals-and-Interference:**

Post-Allowance-and-Post-Issuance:

**Extension-of-Time:**

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
Request for continued examination	1801	1	790	790
<b>Total in USD (\$)</b>				<b>790</b>